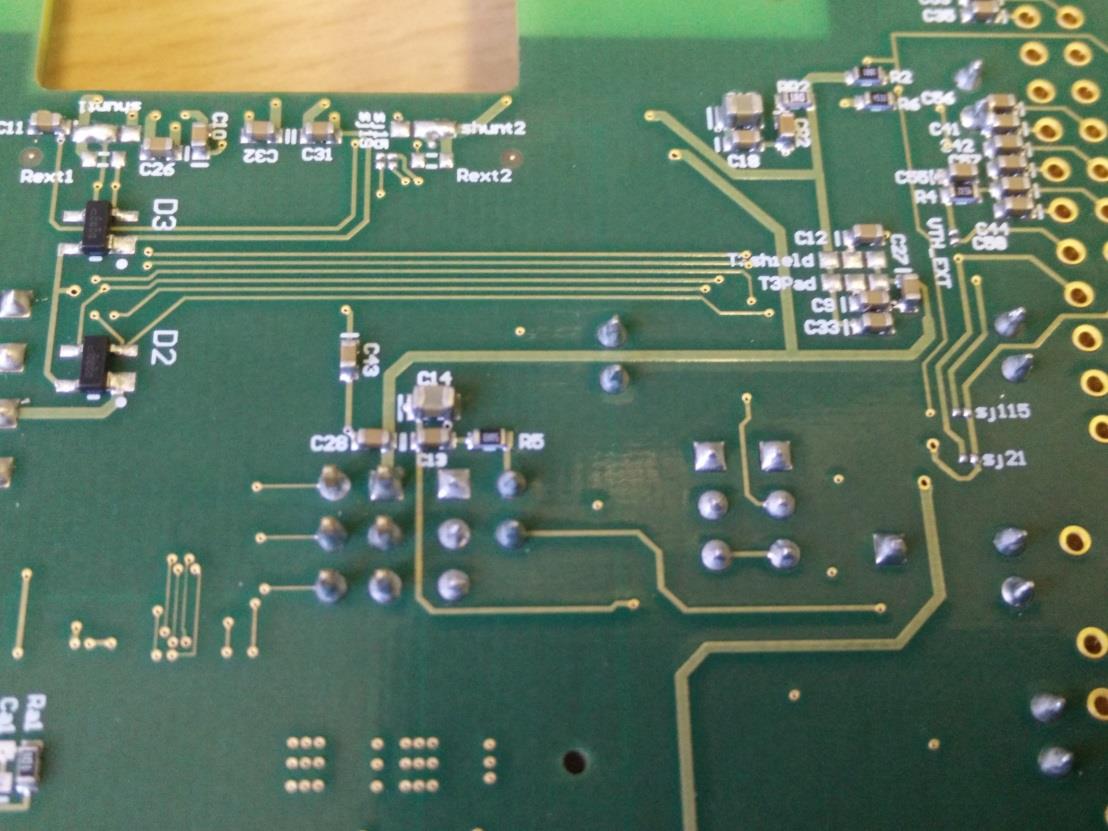
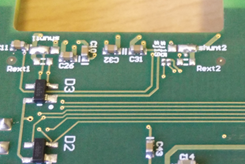
Modifications on the FE-I4 SSC V1.5 for implementing Serial powering in Shunt LDO mode

* VREF1 has to be connected to the BGREFDG (add jumper sj115)
* VREF2 has to be connected to the BGREFAN (add jumper sj19)



* Mount the Rext resistors
  + 5.1K ANALOG- Rext2 (Effective resistor value =3K)
  + 20K DIGITAL –Rext1((Effective resistor value =8.9K)



* Cut traces that connect the pads of capacitors C4, C5, C6, and C7.
* Mount 10nF capacitors for the said capacitors.

