



ATLAS NOTE

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1 **Detector Control System of Tile Calorimeter Low Voltage Power Supplies** 2 **System**

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10 **Abstract**

11 This paper describes the development and implementation of the Detector Control Sys-
12 tem (DCS) of the Low Voltage Power Supplies (LVPS) of the Tile Calorimeter detector. The
13 DCS must ensure coherent and safe operation of the LVPS system, which is one of the main
14 systems of Tile Calorimeter. It provides control and monitoring of all parameters of the
15 system and gives to the user a comprehensive picture of the detector behavior.

16 1 Introduction

17 The Tile Calorimeter [1] is one of the sub-detectors of the ATLAS experiment [2] [3]. It is a sampling
18 calorimeter made of steel plates (absorber) and scintillating tiles (active material). The design, general
19 features and expected performance of the Tile Calorimeter are well described in [1]. The Tile Calorime-
20 ter consists of one barrel and two extended barrel parts. All the three sections have a cylindrical structure
21 further sub-divided into 64 independent modules. The calorimeter cells are defined by grouping together
22 sets of optical fibers into bunches leading to photomultiplier tubes (PMTs). The front-end electronics and
23 PMTs are located in the outer section of the Tile Calorimeter modules, in so-called electronics drawers.

24

25 The Detector Control System (DCS) is responsible for safe and coherent detector operation. All
26 ATLAS sub-detectors have their own local DCS, whose detailed architecture depends strongly on the
27 structure of the general DCS system of the ATLAS experiment and on electronics architecture and me-
28 chanical issues of the sub-detector itself. Each local DCS controls and monitors the operation of a
29 sub-detector and related equipment.

30

31 Although each sub-detector is responsible for the implementation and for internal organization of its
32 sub-systems, it must fully comply with the requirements defined by ATLAS central DCS [4]. The DCS
33 of the sub-detectors must follow the general ATLAS DCS system architecture as much as possible unless
34 there are special requirements where the sub-detectors need tailored solutions.

35

36 The DCS provides control and monitoring of the main systems of the Tile Calorimeter detector,
37 which are the High Voltage distribution system and the Low Voltage Power Supply (LVPS) system. In
38 addition, DCS is responsible for communications with detector calibration and data acquisition systems,
39 and monitoring the detector infrastructure related systems: detector water-cooling and rack control.

40

41 2 Back End system of the Tile Calorimeter DCS

42 The commercial Supervisory Control And Data Acquisition (SCADA) package PVSS II has been chosen
43 by the Joint Controls Project (JCOP) at CERN to implement Back End (BE) software for all LHC ex-
44 periments [5]. It is used to connect to hardware devices, acquire data from them, monitor their behavior
45 and to initialize, configure and operate them. PVSS II has a highly distributed and flexible architecture,
46 and it allows connection of several autonomous systems via the network.

47

48 The BE system of the ATLAS experiment is organized hierarchically in three layers or levels as
49 shown in Figure 1. This hierarchy allows the experiment to be divided in independent partitions, which
50 have the ability to operate in standalone or integrated mode.

51

52 At the top layer, there are Global Control Stations (GCS), which are in charge of overall operation of
53 the detector. They provide high level monitoring and control of all sub-detectors, while data processing
54 and command execution are handled at the lower levels. The GCS is available to access all stations in
55 the hierarchy. The Sub-detector Control Station (SCS) represents the middle level of the hierarchy. The
56 Tile Calorimeter, as a sub-detector of ATLAS, has its own SCS, which allows the complete operation of
57 the sub-detector, by means of dedicated graphical interfaces. At this level of hierarchy, the connection
58 with the TDAQ system, calibration systems and detector infrastructure takes place in order to ensure that
59 detector operation and physics data taking are synchronized. The bottom level of the hierarchy is made
60 up of Local Control Stations (LCS), which handle the low level monitoring and control of LV and HV

61 systems of the sub-detector. The LCS executes the commands received from the layers above.

62

63 In order to implement the BE system of the Tile Calorimeter DCS, five rack-mounted computers are
64 used, located in racks in the ATLAS electronics area underground (USA15). As it is shown in Figure 1,
65 four of those are used as LCS stations (each for one Tile Calorimeter partition) and one as the SCS
66 station. The operating system of those computers is Windows XP and they run PVSS II as a system
67 service.

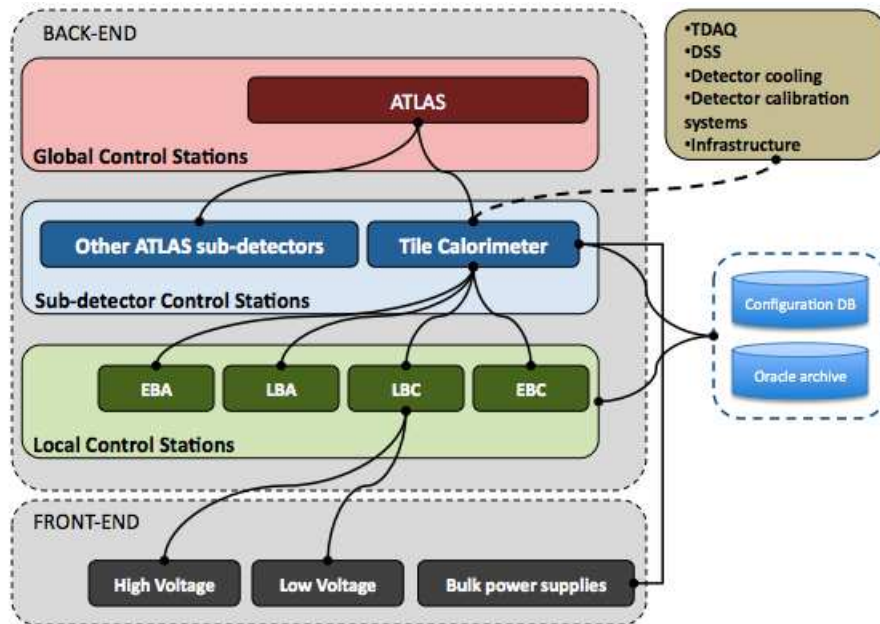


Figure 1: Hierarchy of the DCS of the Tile Calorimeter, as part of the ATLAS control system

68 3 The Low Voltage Power Supplies (LVPS) System

69 The LVPS system is a two stage system: the first stage converts 400V AC input into 200V DC output
70 in USA15 and then the second stage placed on the detector converts 200V DC into 8 independent levels
71 of lower voltages in the range (-15V; +15V) used to power the detector Front End (FE) electronics (the
72 digital and analog components of the readout system and High Voltage (HV) distribution system). In this
73 section, the hardware entities of the LVPS system and their interconnections are briefly described.

74 3.1 Description of the device units of LVPS system

75 The LVPS system is composed of three devices: finger Low Voltage Power Supplies (fLVPS) located at
76 the FE electronics of the Tile Calorimeter, auxiliary boards (AUX boards) located in racks in USA15 and
77 bulk power supplies providing 200V DC located also in the racks of USA15.

78

79 **200V Bulk Power Supply (200V PS)** - uses 400V AC input and produces 200V DC output, which
80 is used as an input to the fLVPS devices. The 200V PS devices are located in racks in USA15. Each unit
81 has 3 output channels and each channel feeds four fLVPS. The nominal output voltage from a channel is
82 200V, with nominal consumption of 4A and 5A for the FE electronics of the Extended and Long Barrel

83 of the Tile Calorimeter, respectively (maximal output current 8.5A).

84

85 **Finger LVPS (fLVPS)** - is a DC-DC converter device, located at the Tile Calorimeter FE electronics.
86 It uses 200V DC as input and converts it into eight different output voltages, from 3.3 to ± 15 V, with a
87 maximum fluctuation of $< 0.01\%$. Eight DC-DC converter bricks placed inside this device are grouped
88 into two groups. One group supplies the PMT high voltage distributor system, while the other feeds the
89 rest of the FE electronics. Those two sets of DC-DC converter bricks are called LV-MB and LV-HV
90 side bricks. The list of individual DC-DC converter brick names, together with their operational output
91 ranges, is given in Table 1.

DC-DC converter bricks	V out range [V]	I out range [A]
LV-MB bricks		
3.3VDIG	3.3 - 3.9	1.7 - 5.6
5VDIG	5.0 - 5.9	3.3 - 6.7
5VMB	5.0 - 5.9	6.6 - 13.3
-5VMB	5.0 - 5.9	3.3 - 6.7
15VMB	14.4 - 15.65	0.2 - 0.6
LV-HV bricks		
5VHV	5.0 - 5.9	0.1 - 0.3
15VHV	14.4 - 15.65	0.15 - 0.4
-15VHV	14.4 - 15.65	0.15 - 1.9

Table 1: List of DC-DC converter bricks of the fLVPS device and ranges of their voltages and currents output

92 **Auxiliary board (AUX board)** - provides operational voltages for each fLVPS device and switches
93 off its output voltages by disabling current loops. The AUX board has four sets of output voltages and
94 current loops, each dedicated to one fLVPS device and one more interlock current loop. The AUX board
95 output voltages and currents are summarized in Table 2 and its connection to other LVPS system devices
96 can be seen in Figure 2. The purpose and functionality of the AUX boards are the following:

- 97 • To provide power for the Analog input (Ai) part of the ELMB (see Section 4.2) and its mother
98 board placed inside the fLVPS device. (The separate power line for ELMB gives the possibility to
99 power cycle it in case of readout problems without disturbing supply to the FE electronics).
- 100 • To provide two current loops per fLVPS, which enable or disable output voltages separately for
101 LV-MB or LV-HV sets of DC-DC converter bricks.
- 102 • To provide a short (~ 1 sec) start-up pulse to switch on the DC-DC converters. When the DC-DC
103 converters start to produce output voltage the start-up pulse is no longer needed.
- 104 • To produce current loops for the LVPS system hardware interlock (hardware interlocks are not
105 discussed in this note, since they do not belong to the DCS system)

106 The total number of the LVPS system device units, used for the Tile Calorimeter are the following:
107 256 fLVPS, 64 AUX boards and 22 units of 200V Bulk Power Supplies.

108 3.2 Schematics of interconnection of LVPS system devices

109 This section briefly describes the schematics of the connections between the devices of the LVPS system
110 for powering purposes. As it is shown in Figure 2, there are long supply lines from the ATLAS electron-

Channel	V out [V]	I out [mA]
Supply for ELMB Ai (of fLVPS)	12	30
Supply for MB (of fLVPS)	12	30
Start-up pulse	12-18	30
LV-MB current loop	–	10
LV-HV current loop	–	10

Table 2: List of output voltages and currents of AUX board devices

111 ics cavern to the detector of the order of 120-150m. Those supply lines are used to provide necessary
 112 voltages for operation of the fLVPS devices.

113

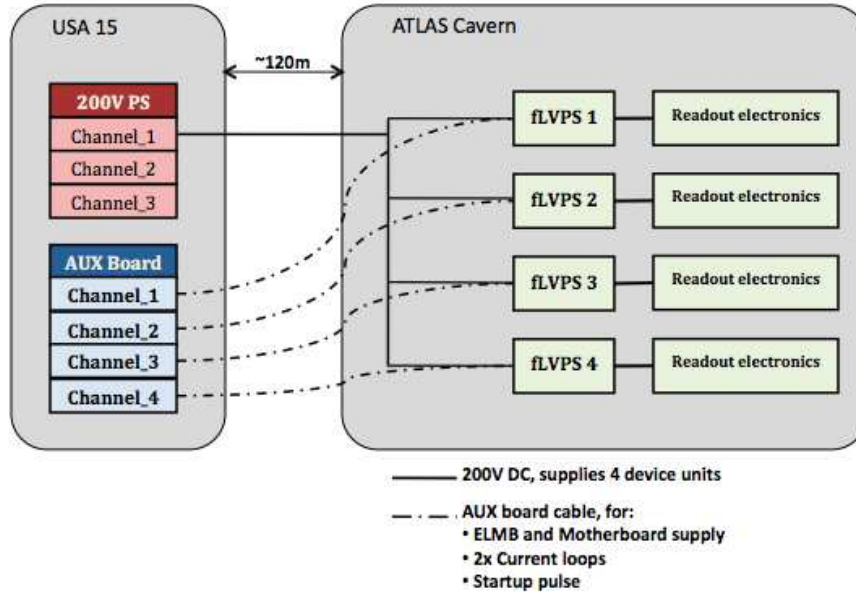


Figure 2: Schematics of supply cables interconnection in LVPS system

114 The AUX board has four channels, each of them is connected to a single fLVPS device, i.e. one AUX
 115 board supplies a sequence of four fLVPS devices. Similarly, each of the three channels of a 200V DC
 116 bulk power supply feeds four fLVPS devices with DC power.

117

118 4 Communication schemas and types of LVPS system devices

119 The communication protocols used in the LVPS system and hardware connections between DCS com-
 120 puters and controlled devices are briefly described in this section. Devices of the LVPS system support
 121 two different types of communication protocol: for 200V PS devices the serial Modbus [6] communi-
 122 cation is used; for readout and control of AUX boards and fLVPS devices the CANbus [7] protocol is
 123 used.

124 **4.1 Communication with 200V PS**

125 PVSS II provides a driver for the TCP variant of the Modbus protocol. The power supply side supports
 126 the serial Modbus/RTU protocol. In order to interface the 200V PS device with SCADA software in a
 127 flexible way, an intermediate device was incorporated as a part of DCS. It acts as the converter of Mod-
 128 bus/TCP to Modbus/RTU and as is shown in Figure 3. Communication with the 200V Bulk PS can be
 129 established through the TCP/IP network. In this way the PC connects to the device through the Ethernet
 130 card.

131

132 As an intermediate device, the Port Server TS MEI was chosen [8]. It is a commercial product,
 133 designed for universal and high-performance serial-to-Ethernet connectivity. It is ideal for RS 422 appli-
 134 cations where remote device management and control are required.

135

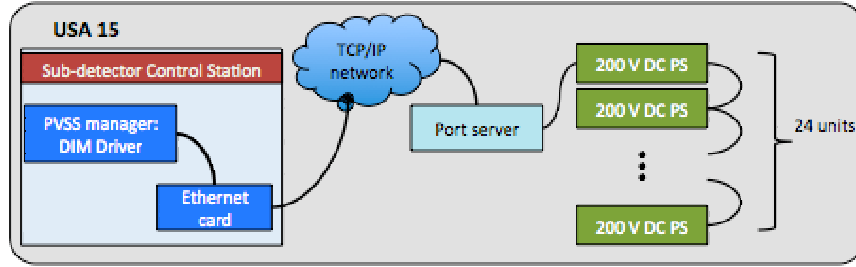


Figure 3: 200V Bulk PS communication schema

136 As it is shown in Figure 3, communication with 200V PS device units is established from the SCS
 137 (see chapter 2 and Figure 1). For the Tile Calorimeter sub-detector, the total number of 200V PS device
 138 units to be controlled is 22. These power supplies are daisy chained and connected to the port server.

139

140 The mapping of addresses between PVSS II and the 200V PS device units is defined in the configu-
 141 ration of the PVSS II Modbus driver, where the 200V PS device units are interpreted by the PVSS II as
 142 the Programmable Logic Controllers (PLC). Table 3 shows the parameters of Modbus driver, defined in
 143 the project running at the SCS. The node addresses of the device units are set manually directly on the
 144 front panels of the device units.

145

Parameter	Value
PLC	_Mod_Plc_[PLC No]
PLC number	11...16; 21...26; 31...36; 41...46
Unit address	11...16; 21...26; 31...36; 41...46
Modbus transaction timeout	10 sec
Frame coding	TCP
Endianness	Big Endian
List of Hosts	IP address of Port Server

Table 3: Communication properties of Modbus/TCP driver

146 4.2 Communication with AUX board and fLVPS devices

147 The fLVPS and AUX board devices make use of the Embedded Local Monitoring Board (ELMB) [9], as
148 a general purpose I/O and processing unit for CAN communication. The ELMB fully conforms to the
149 industry standard of CANbus protocol and it provides the minimal functionality of a slave node according
150 to the specification of this protocol. The configuration of the Analogue-to-Digital Converter (ADC) used
151 in the ELMBs in the LVPS system is given in Table 13 in appendix A.

152 Figure 4 shows the layout of the ELMB based readout chain, for one Tile Calorimeter partition. In
153 the LVPS system the maximum number of ELMB nodes per CAN branch is 16 and the number of the
154 CAN branches per partition is 5. Four of the CAN branches are used for communication with fLVPS
155 devices and one for communication with the AUX boards. The CAN Power Supply Unit (CAN PSU)
156 is used to feed the CAN Transceiver part of the ELMB. The length of the CAN branches, used to commu-
157 nicate with the fLVPS devices, is 120-150m and for the AUX board devices it is ~ 10 m.

158
159 The CAN communication speed for the AUX board and fLVPS devices is set to 125KB/sec. Used
160 CAN node addresses are from 1 to 16, as labeled in Figure 4. Branch #0 serves for the communication
161 with the AUX devices and the branches #1 - #4 for the fLVPS devices. The numbering of Kvaser card
162 port corresponds to the branch numbering.

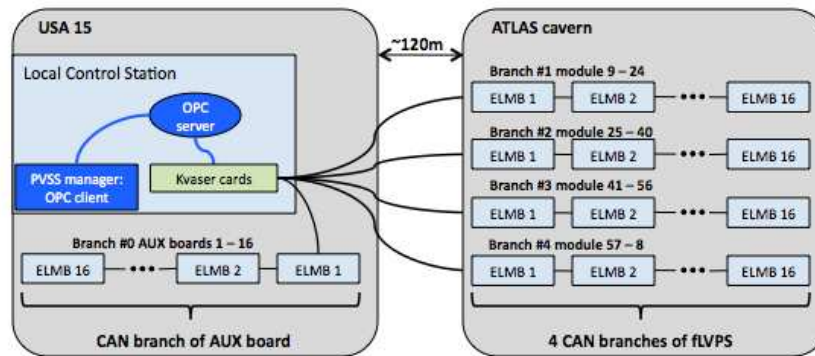


Figure 4: Communication schema with AUX boards and fLVPS devices (for one Tile Calorimeter partition)

163 To interface the ELMB based FE systems with the back-end SCADA software PVSS II, dedicated
164 middle-ware software was developed by the ATLAS DCS central team, the CANopen OPC server [10],
165 which is based on OPC [11] standards. A dedicated OPC client is provided by the PVSS II manager.

166
167 The OPC server is used to acquire raw data and to send commands to the devices. It does the conver-
168 sion of raw data to the physical units (to voltages, currents and temperatures). The OPC configuration
169 file (OPCCanServer.cfg) contains the information needed to define the address space of the OPC server
170 and formulas for conversion of the acquired raw data to the physical units (voltages, currents and tem-
171 peratures).

172
173 Below, two standardized CAN communication objects are summarized and their relevance to the
174 detector control outlined:

- 175 • Process Data Objects (PDO) are broadcast and unconfirmed messages containing up to 8 data
176 bytes. This mechanism is used for real-time transfers, with one sender and one or more receivers.
177 The PDO messages are used for monitoring purposes.

178 • Service Data Objects (SDO) are confirmed transfers of any length. Peer-to-peer communication is
 179 established between two nodes of the network by this mechanism. The SDO messages are used to
 180 transmit configuration commands to ELMBs.

181 The Analog inputs (A_i) to the ELMB (i.e. monitored parameters of the fLVPS and AUX board de-
 182 vices) can be read out using PDO messages. The so-called SYNC command triggers the ELMB to start
 183 the A_i channel scan and to send out up to 64 PDO messages, one for each analog input channel. With
 184 an ADC frequency of 60 Hz it takes $\sim 1-2$ sec to scan all the ELMB A_i channels, see more details in
 185 appendix A.

186
 187 SDO messages are used to transmit commands to the ELMBs of the fLVPS and AUX board devices.
 188 The list of implemented commands for fLVPS and AUX board devices are given in Table 4, together with
 189 the respective CAN object properties. For the AUX board commands on the *Start – up* pulse, the set of
 190 values in last column define the width and amplitude of the generated pulse. For the fLVPS device, the
 191 output voltages are adjusted by the values whose allowed ranges are given in the last column. Commands
 192 to the fLVPS and AUX boards via the respective ELMBs are processed in the following way:

- 193 • **AUX boards** - by sending SDO messages to the ELMB, the DAC registers of the Maxim 6957
 194 chip can be accessed in order to trigger actions that are defined by the CAN object index. The
 195 Maxim chips are located on the Motherboard of the AUX board. The ELMB has access to the
 196 Motherboard of the AUX board through its Serial Peripheral Interface (SPI).
- 197 • **fLVPS** - by sending SDO messages to the ELMB, the DAC registers of the Maxim 525 chips
 198 located on the Motherboard of the fLVPS device can be accessed. The output voltages of individual
 199 DC-DC converter bricks can be adjusted by writing the appropriate values in the DAC registers,
 200 via the Digital output (Do) of ELMB.

AUX board channel	Command	CAN object index	Value(s) to send
ELMB supply	ON/OFF	2701/2702H (0)	0x04010N
ELMB-MB supply	ON/OFF	2701/2702H (0)	0x07020N
MB current loop	ON/OFF	2701/2702H (0)	0x4F040N
HV current loop	ON/OFF	2701/2702H (0)	0x4F050N
Start-up pulse	Set width	2700H	0x0 - 0x12
Start-up pulse	ON amplitude	2705H	0x0 - 0x7
Interlock current loop	ON	2704H	0x2F
fLVPS channel			
Digitizer 3.3	Set min-max	6411H (1)	4095 - 0
Digitizer 5V	Set min-max	6411H (2)	4095 - 0
Motherboard 5V	Set min-max	6411H (3)	4095 - 0
Motherboard -5V	Set min-max	6411H (4)	4095 - 0
Motherboard 15V	Set min-max	6411H (11)	4095 - 0
HV supply 5V	Set min-max	6411H (12)	4095 - 0
HV supply 15V	Set min-max	6411H (13)	4095 - 0
HV supply -15V	Set min-max	6411H (14)	4095 - 0

Table 4: List of commands and parameters that can be send to the ELMBs of the AUX boards and fLVPS. (The Set width and Initialization of AUX board are equivalent commands see section 5.3).

201 On the CANbus, PDO messages have higher priority than SDO messages, as defined by the arbitra-
 202 tion collision mechanism of the CANbus protocol. In order to avoid delays (or possible timeout) of SDO
 203 messages, the ELMBs that should receive an SDO message are first set into pre-operational mode. In
 204 the pre-operational mode the ELMB stops sending out PDO messages and becomes available to receive
 205 SDO commands.

206 5 Monitoring and Commands for LVPS system devices

207 The DCS of the Tile Calorimeter provides constant monitoring of the LVPS system parameters and
 208 provides comprehensive information about the detector behavior. Critical parameters of the system are
 209 always checked to see if they are within allowed range, otherwise WARNING or ALARM are triggered.

210 5.1 Monitoring of 200V PS devices

211 The parameters of the 200V PS are monitored at intervals of 20 sec. This time interval can be defined
 212 separately for each Poll group (one per device unit). For monitoring, all the read only registers are polled,
 213 which include all the ones used for Input Status and Input Register.

214 The number of monitored parameters per device unit is 44, where 42 of them characterize individual
 215 output channels (3 channels per device unit) and the rest describe the Power Supply global parameters.
 216 Table 5 shows the monitored parameters of the 200V PS devices, together with their output ranges.

217
 218 Temperature probes are located at each output channel of the 200V PS device. A dedicated PVSS II
 219 script averages the values read out from the temperature probes of one device unit and triggers WARN-
 220 ING or ALARM if the temperature is outside the allowed range. Additionally, the script marks channel
 221 trips, as the power supplies have no dedicated register for this.

222

Parameter	Range
Crate On/Off Status	0: Off; 1: On
Crate remote control	0: Disabled; 1: Enabled
3x Channel Output	0: Off; 1: On
3x Channel Mode	0: Manual; 1: Automatic
3x Channel On/Off Status	0: Off; 1: On
3x Channel Error Status	0: OK; 1: Hardware Error
3x Channel Over Voltage	0: OK; 1: Over Voltage
3x Channel Over Current	0: OK; 1: Over Current
3x Channel Fuse	0: OK; 1: Error
3x Channel Status	0: Offline; 1: Online
3x Channel Interlock	0: Open; 1: Close
3x Channel HW error	0: OK; 1: Error
3x Channel Output voltage (V)	1 ... 2500 (*0.1 Volt)
3x Channel Output current (I)	1 ... 1000 (*0.001 Amp)
3x Channel Sense line voltage (V)	1 ... 2500 (*0.1 Volt)
3x Channel Temperature	1 ... 900 (*0.1 Celsius)

Table 5: Monitored parameters per 200V PS device unit, together with respective output ranges

223 5.2 Monitoring of AUX board and fLVPS devices

224 Parameters of the AUX board and the fLVPS devices are readout at a frequency that is defined in the
225 configuration of the OPC server, and the default value that was chosen is 10 seconds. By sending SYNC
226 commands to the CANbus branches, the OPC server gets data from the ELMBs at intervals of 10 sec.

227

228 The ELMB has 64 Ai channels and digitizes analog signals at a frequency of 60 Hz (see Table 13
229 in appendix A). The AUX board uses only 37 Ai channels of its ELMB and the fLVPS uses only 55 Ai
230 channels. In other words, 37 parameters are read out from the AUX board device and 55 from the fLVPS
231 device. The number of the LVPS system parameters per partition acquired through the OPC server and
232 processed by the PVSS II every 10 sec is 4112.

233

234 Critical parameters of the AUX board and the fLVPS devices are further analyzed by the PVSS II
235 data-point functions and control scripts, where pre-defined thresholds are used to trigger WARNING or
236 ALARM. Tables 6 and 7 show the list of monitored parameters of the AUX board and the fLVPS devices,
237 together with the associated ELMB channels and allowed ranges.

238

Channel	Allowed range
(4x) Temperature probes	> 60/70 (°C) : Warning/Alarm
(4x) ELMB voltage	> 10 (V) : OK
(4x) ELMB current	> 25 (mA) : OK
(4x) MB voltage	> 10 (V) : OK
(4x) MB current	> 25 (mA) : OK
(4x) Start-up pulse voltage	-
(4x) Start-up pulse current	-
(4x) MB current loop	> 7 (mA) : OK
(4x) HV current loop	> 7 (mA) : OK
Interlock current	> 25 (mA) : OK

Table 6: List of AUX board channels, together with the associated ELMB Ai channel. The most relevant allowed ranges are shown. The thresholds for temperature warnings/alarms are applied to the averaged measurement of all four probes

239 5.3 Implemented commands for LVPS system devices

240 The DCS of the LVPS system provides a comprehensive set of commands in order to operate the system
241 and to provide correctly the low voltage for Tile Calorimeter FE electronics. Properties associated to the
242 commands implemented for devices of the LVPS system are described in this section. The command
243 libraries were written in the PVSS II programming language.

244

245 200 V DC PS commands:

- 246 • **Crate Local Control** - Enables or disables local control of the Power Supply
- 247 • **Channel ON/OFF** - Switch ON/OFF channel of 200V PS
- 248 • **Set voltage** - Set output voltage

Channel	Allowed range
3V Brick input voltage	-
(4x) $\pm 5V$ Brick input voltage	-
(3x) $\pm 15V$ Brick input voltage	-
3V Brick input current	-
(4x) $\pm 5V$ Brick input current	-
(3x) $\pm 15V$ Brick input current	-
3V Brick output voltage	2.5 - 3.6 : OK
(4x) $\pm 5V$ Brick output voltage	4.5 - 5.8 : OK
(3x) $\pm 15V$ Brick output voltage	13.15 - 14.5/14.8/15.5 : OK
3V Brick output current	-
(4x) $\pm 5V$ Brick output current	-
(3x) $\pm 15V$ Brick output current	-
3V Brick sense line voltage	-
(3x) $\pm 5V$ Brick sense line voltage	-
15V Brick sense line voltage	-
(2x) Water temperature probes	-
(16x) Brick temperature probes	> 50/60 Warning/Alarm

Table 7: List of the fLVPS device parameters. The most relevant allowed ranges are shown (the upper limit of 15V Bricks varies from 14.5 to 15.5). The sense lines are not implemented for all types of output voltages.

249 • **Set current limit** - Set limit on output current

250 • **Set high/low sense limit** - Set high/low limit on sense line voltage measurement

251 **AUX board commands:**

252 • **Initialization** - sets the width for the Start-up pulse (see section 2). With this command the device
253 AUX Board goes to its initial state, i.e. all output channels are switched OFF and becomes ready
254 to accept commands via CAN bus.

255 • **Channel ON/OFF** - to switch ON/OFF any output channel of the AUX board device (see list of
256 output channels in Table 2).

257 • **Partition recover** - takes an action on all AUX board devices of one partition. It sends the
258 *Initialization* command to un-initialized AUX boards and switches ON the ELMB and the MB
259 supply channels of all AUX board devices.

260 **fLVPS commands:**

261 • **Set output voltage** - sets output voltages of individual output channels, within allowed ranges.
262 The DAC constants are in inverse correlation with output voltages, i.e. minimum DAC constant
263 corresponds to maximum output voltage.

264 • **Switch on fLVPS** - enables output voltages from fLVPS devices. During this action several com-
265 mands are sent to the ELMBs of the fLVPS and AUX board devices, in the sequence described in
266 Table 8.

267 At step 8 shown in Table 8, a time delay is implemented in order to ensure stabilization of output
268 voltages from fLVPS device and only after that the output voltages are tuned to nominal values.

	Command/action	Time delay
1.	Check communication with the ELMB of fLVPS	-
2.	Load Calibration constants from Configuration DB	-
3.	Set output voltages to minimum	Wait for the ELMB reply
4.	ON HV-LV current loop	
5.	ON Start-up pulse	3 sec
6.	ON MB-LV current loop	-
7.	ON Start-up pulse	3 sec
8.	Stabilization time	40 sec
9.	Ramp-up output voltages to nominal	5 sec

Table 8: Queue of actions taken during switch on of a fLVPS device. Some of the actions cause significant time delays as shown.

5.4 Sequence and synchronization of commands

In this section we describe the sequence of actions (commands), which should be followed in order to provide low voltage for the Tile Calorimeter FE electronics. This sequence of actions is defined by the hardware configuration of the LVPS system devices. Table 9 shows the sequence of commands in their right order of execution and time estimates for their execution.

	Command	Time per partition
1.	Partition recover	~ 1 min
2.	Switch ON 200V PS channels	~ 1 min
3.	Switch ON fLVPS output channels	~ 12 min

Table 9: The switch on commands of the different components of the LVPS system in the correct sequence and estimated time for their execution

The DCS commands can be launched either at the level of single device units or at higher levels using actions implemented at the Finite State Machine (FSM) [12] on a sub-detector module, partition or detector. On the level of a single device unit, it takes ~1 min to power-up to nominal output voltage one fLVPS device unit (including stabilization time). At higher levels, commands are executed in parallel for multiple devices.

Synchronization of commands becomes necessary at the point when commands to different devices, connected through the same or different CAN branches, should arrive one after another within hardware specific time delays. Such a situation appears during the command Switch ON fLVPS, between steps 3 - 7 described in Table 8. In order to avoid possible conflicts of SDO/PDO messages, and to avoid delays in command delivery, the synchronization mechanism is implemented on the level of PVSS II functions. In other words, on each CAN branch, SDO messages can be sent only to one device unit at a time.

6 Usage of Configuration and Conditions DB

Two types of databases are used in the Tile Calorimeter DCS - Configuration and Conditions databases. The Configuration database is used to store the data related to the detector configuration. The Conditions

290 database is used to store information for understanding the detector behavior, such as voltages, currents
291 and temperatures of devices.

292 6.1 Configuration DB

293 The Configuration DB is used to store the output voltage calibration parameters and the DAC parameters
294 that correspond to the nominal output voltages of the fLVPS devices:

- 295 • **Calibration parameters** - are used to correct output and sense line voltage measurements from
296 the fLVPS, using tests performed at the laboratory. These constants are applied at the PVSS II
297 software level, using the message conversion mechanism of data-point elements. The distribution
298 of the currently used calibration constants is given in Figure 5.
- 299 • **DAC parameters** - numbers to be sent to the fLVPS device in order to set nominal output voltages.

300 At the assembling location, the fLVPS devices are tested and certified, which includes finding cali-
301 bration and nominal DAC settings for output voltages. Calibration and DAC parameters are saved in the
302 Configuration DB, together with the serial number of the ELMB of the tested fLVPS device. The serial
303 number of the ELMB is unique and used to find calibration and DAC constants for the appropriate fLVPS
304 device in the Configuration DB. During operation at the detector, the DCS uses the serial number of the
305 ELMB to load calibration and DAC parameters of the appropriate fLVPS device from the Configuration
306 DB.

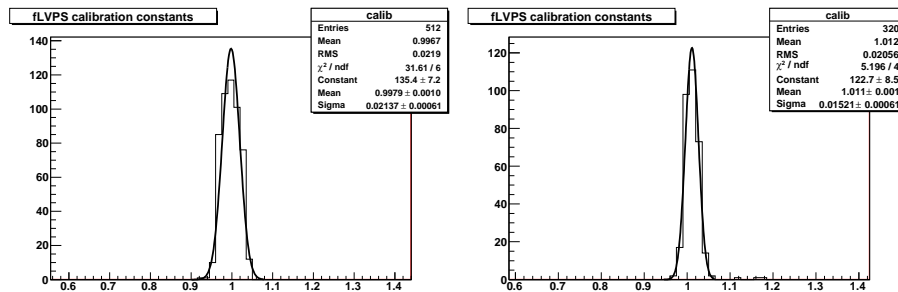


Figure 5: Distributions of calibration parameters for measured output (left) and sense line (right) voltages, for all fLVPS of one of the Tile Calorimeter partition

307 6.2 Conditions DB and data smoothing

308 The DCS acquires data from the fLVPS and the AUX board devices every 10 sec, and from the 200V
309 Bulk PS every 20 sec. However, in stable detector working conditions most of the DCS data changes
310 very little with time and there is no need to save all this data into the database. In order to save disk space
311 and bandwidth, and allow faster access to the data, a smoothing filter is applied to the DCS data. There
312 are several types of smoothing applied for saving LVPS system parameters into the database, and they
313 work in the following way:

- 315 • **Threshold and Time** - makes a comparison between old and new data and does not save it into
316 the database if the difference is smaller than a pre-defined threshold. Data will be saved in any
317 case after a given time interval

318 • **Old/new and Time** - makes comparison between old and new data, and does not save it into the
 319 database if data remains unchanged. Data will be saved in any case after a given time interval.

320 Tables 10 - 12 list the parameters archived for the devices of the LVPS system (200V Bulk PS, AUX
 board and fLVPS) together with the types of smoothing and their parameters.

Parameter	Smoothing type	Time (sec)	Threshold
Input voltages	Value and time	3600	± 1 V
Input current	Value and time	21600	± 0.2 A
Output voltages	Value and time	3600	± 0.05 V
Output current	Value and time	3600	± 0.05 A
Temperature	Value and time	3600	$\pm 1^{\circ}\text{C}$
DAC constants	Old/new and time	21600	-
State	Old/new and time	21600	-

Table 10: Smoothing settings of the archived parameters of fLVPS devices

321

Parameter	Smoothing type	Time (sec)	Value
ELMB current	Value and Time	18000	± 0.2 mA
ELMB voltages	Value and Time	18000	± 0.2 V
ELMB-MB current	Value and Time	18000	± 0.2 mA
ELMB-MB voltages	Value and Time	18000	± 0.2 V
Temperature	Value and Time	3600	$\pm 1^{\circ}\text{C}$

Table 11: Smoothing settings of the archived parameters of the AUX board devices

Parameter	Smoothing type	Time (sec)	Value
Channel Voltage	Value and Time	3600	$\pm 0.5\text{V}$
Channel Current	Value and Time	3600	$\pm 5\%$
Channel Temperature	Value and Time	3600	$\pm 1.0^{\circ}\text{C}$

Table 12: Smoothing settings of the archived parameters of 200V PS devices

322 The frequencies of records for the parameters (voltages, currents and temperatures) of the fLVPS
 323 devices have been analyzed. The results are shown in Figure 6, which shows the frequencies of records
 324 into the Conditions DB during a one month period, for individual monitored parameters of the fLVPS
 325 devices. For stably working electronics, it is expected to have an average of one record during the time
 326 interval that is specific for the applied smoothing type (see smoothing definitions and Tables 10 - 12).
 327 Such a plot could be very useful to understand the detector behavior from the DCS point of view and
 328 easily visualize instabilities of the electronics.

329

330 During the one month period used, the read out of some of the monitored parameters stopped from
 331 time to time, usually due to communication problems, resulting in a few parameters showing values
 332 below one record per hour in Figure 6. Frequencies clearly above one per hour are caused by the big
 333 variation of monitored values of the output currents of a few modules/bricks, and will be investigated
 334 further.

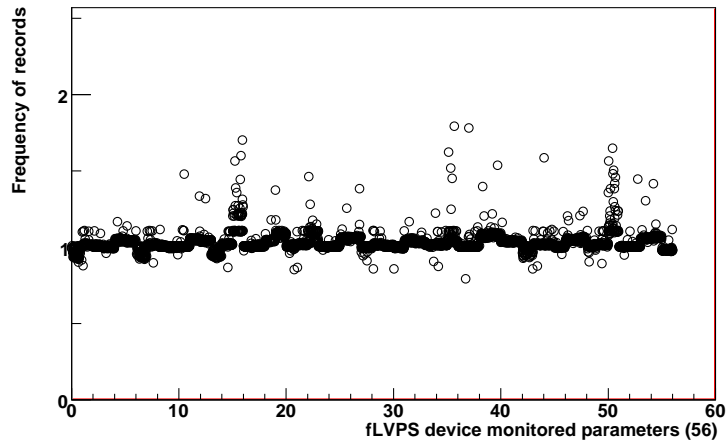


Figure 6: Frequencies of records (per hour) of the parameters of fLVPS device units.

7 Conclusions

This paper presents a comprehensive picture of the Tile Calorimeter DCS system, as implemented following the requirements of the ATLAS DCS integration guidelines. The DCS of the LVPS system has been ready on time and proved to be both user-friendly and robust. It was successfully operated in a reliable manner for almost 2 years. A key element in this successful implementation was the correct selection of the building blocks since the beginning of the implementation.

We present the organization of the supervisory level for the LVPS system, information necessary for the operator to have the ability of full control over the system, and a detailed description of the hardware components of the LVPS system with emphasis on the critical parameters of the system and their monitoring thresholds for ALARM and WARNING. Details of the commands implemented for the individual device units of the LVPS system are given as well as time estimates for their execution.

Usage of the Configuration and Conditions DB are also described in this paper. Analysis of the DCS data from the Conditions DB showed that the daily recorded data is reasonable in size and allows understanding of the LVPS system behavior. Implementation of the Configuration DB allows storing of the full information about the LVPS system calibration and nominal output voltages.

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364 their fruitful cooperation with the developers of the DCS. Their selfless and rigorous work helped us in
365 development and successful implementation of the LVPS system DCS.

366 **References**

- 367 [1] *ATLAS Collaboration, Tile Calorimeter Technical Design Report*, CERN/LHCC 96-42, CERN,
368 1996.
- 369 [2] *ATLAS Collaboration, Atlas technical proposal, Technical Design Report*, CERN, 1994.
- 370 [3] *ATLAS Collaboration, The ATLAS experiment at the CERN Large Hadron Collider*, JINST 3 (2008)
371 S08003.
- 372 [4] *ATLAS DCS Integration Guidelines*, ATLAS-DQ-ON-0013, EDMS Id: 685451, CERN, 2007.
- 373 [5] *A. Daneels and W. Salter. Selection and Evaluation of Commercial SCADA systems for the Controls*
374 *of the CERN LHC Experiments. International Conference on Accelerator and Large Experimental*
375 *Physics Control Systems* , ICALEPCS, 1999.
- 376 [6] *Modicom, Modbus Protocol, Reference Guide*, PI-MBUS-300 Rev. J, 1996.
- 377 [7] *CAN in Automation (CiA)*, www.can-cia.org.
- 378 [8] *Port Server TS MEI is produced by Digi International (DigiBoard) company*, www.digi.com.
- 379 [9] *H. Boterenbrood, B.I. Hallgren, The Development of Embedded Local Monitor Board (ELMB)*,
380 *ATL-DAQ-2003-053*.
- 381 [10] *OPC CANopen Server User Guide*, ATL-DQ-ON-0007, EDMS Id: 684951, CERN, 2005.
- 382 [11] *OPC*, <http://www.opcfoundation.org>.
- 383 [12] *Hierarchical Control of the ATLAS Experiment / Barriuso-Poy, Alex*, CERN-THESIS-2007-037,
384 *Tarragona Univ.*, 2007.
- 385 [13] *CANopen Application Software for the ELMB128 / H. Boterenbrood*,
386 <http://www.nikhef.nl/pub/departments/ct/po/html/ELMB128/ELMB23.pdf>, 2006.

387 A ELMB analog input settings

388 The configuration parameters for the analog inputs of the ELMB used in the finger LVPS are shown in
389 table 13. All the 64 multiplexed inputs are read by the ADC, at a rate of 60 Hz. In practice the achievable
390 rate of conversions is limited to about 30 Hz maximum due to the slow opto-couplers used in the (serial)
391 interface between the processor and the ADC [13].

ELMB ADC properties	Set
Rate (Hz)	60
Range (V)	5.0
Highest channel number	64
Polarity	Bi-polar
Analog Input Transmission	After SYNC message only

Table 13: Configuration of the ELMB used in the LVPS system

392 B Address field configuration for AUX board initialization

393 The address field configuration for the start-up pulse set command is given in table 14. In the example,
394 it is used to set the parameters of AUXboard12 (initialization of this AUXiliary board unit). The AUX
395 board units need to be initialized before they are used to start-up the finger LVPS. Parameters like the
396 duration of the start-up pulse are set in the initialization.

Parameter	Set value
Server	OPCCANopen
Group	Cmd_AUXboard
Item	AUXboard.AUXboard12.AUXINIT
Transition Type	Integer
Direction	Out

Table 14: Example of address field configuration for start-up pulse set command of one of the AUX boards.

397 **C List of acronyms**

398	ADC - Analog to Digital Converter
399	AUX (board) - Auxiliary board used to control fLVPS
400	BE - Back End
401	CAN - Controller Area Network
402	DAC - Digital to Analog Converter
403	DC-DC - Direct Current to Direct Current
404	DCS - Detector Control System
405	ELMB - Embedded Local Monitor Board
406	FE - Front End
407	fLVPS - finger Low Voltage Power Supply
408	FSM - Finite State Machine
409	GCS - Global Control Station
410	HV - High Voltage
411	JCOP - Joint COntrols Project (for LHC)
412	LCS - Local Control Station
413	LV - Low Voltage
414	LVPS - Low Voltage Power Supply
415	MB - Mother Board
416	OPC - a standard for communication of real time data, originally known as Object Linking and
417	Embedding (OLE) for Process Control
418	PDO - Process Data Object
419	PMT - PhotoMultiplier Tube
420	PS - Power Supply
421	PVSS II - SCADA software used in ATLAS DCS
422	SCS - Sub-detector Control Station
423	SCADA - Supervisory Control And Data Acquisition
424	SDO - Service Data Object
425	TCP/IP - Transmission Control Protocol / Internet Protocol
426	TDAQ - Trigger and Data Acquisition
427	USA15 - Electronics cavern near the ATLAS detector